

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,849,892 B2
DATED : February 1, 2005
INVENTOR(S) : Hideki

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 16,

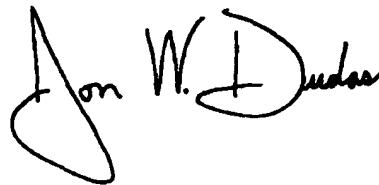
Line 16, should read -- are about half of the width of the transistor active region. --

Line 33, should read -- region and wherein the second storage region protrudes from --

Line 36, should read -- 5. A device according to claim 1 wherein the transistor --

Signed and Sealed this

Seventh Day of June, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office